

Semiconductor Device and Method of Manufacturing the Same

Nemoto et al

US Serial No. 10/083,673

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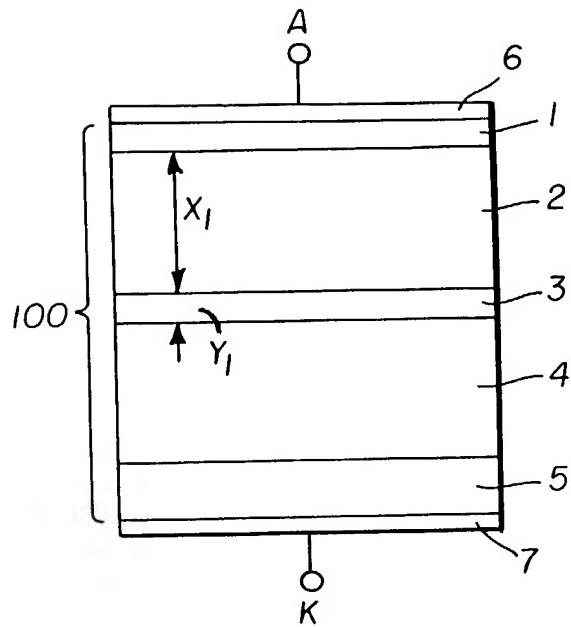


FIG. 1

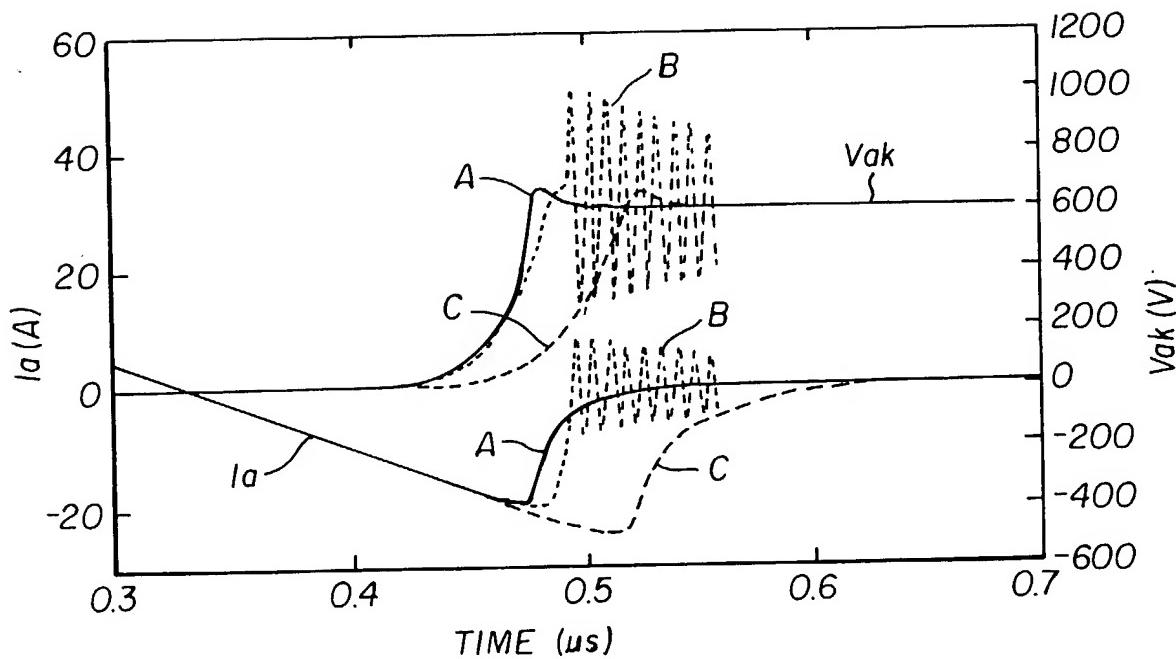


FIG. 2

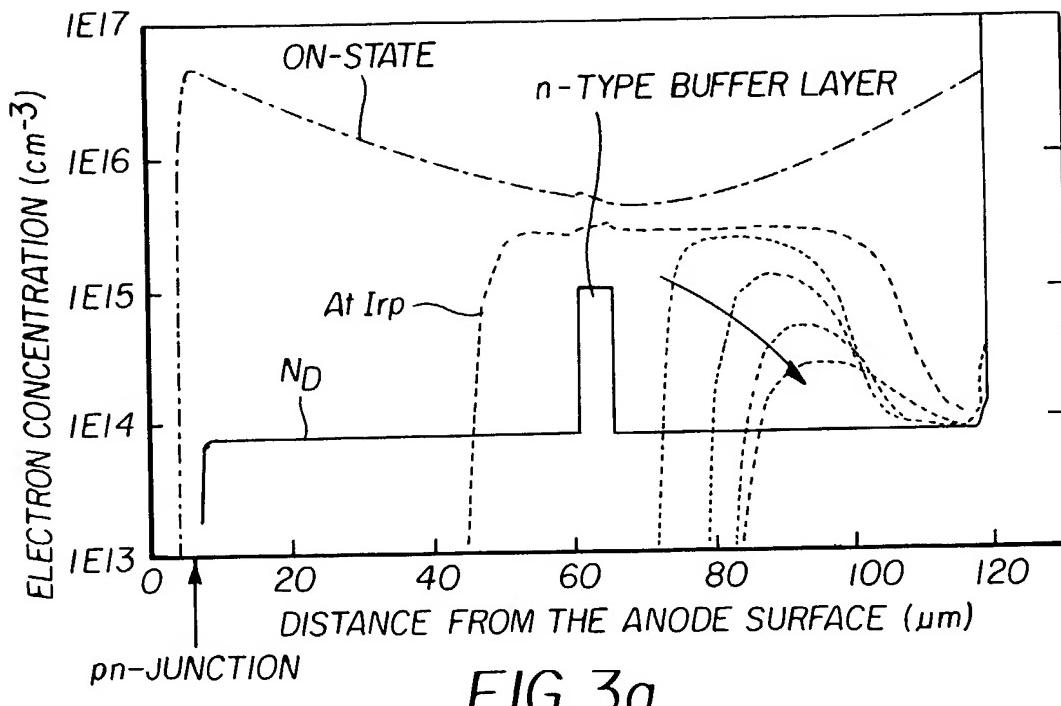


FIG. 3a

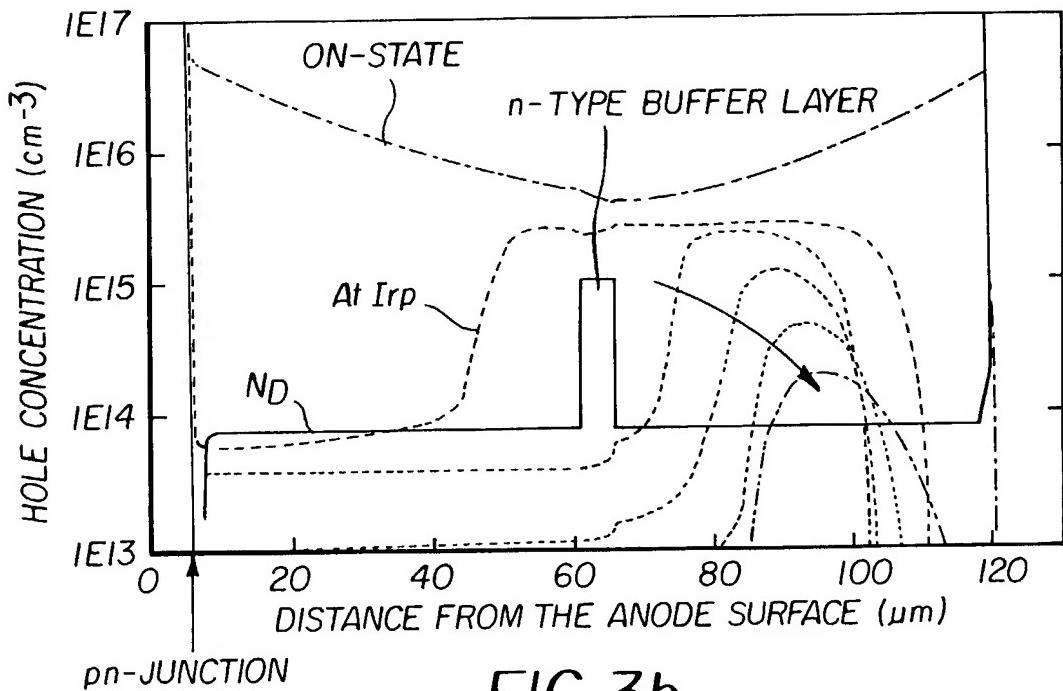


FIG. 3b

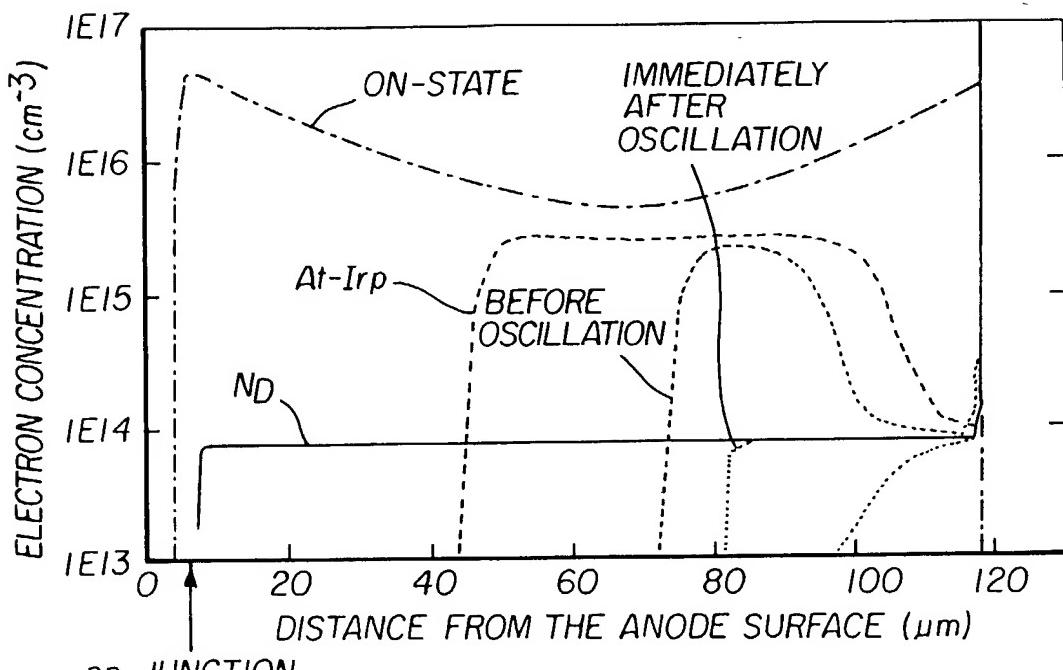


FIG. 4a

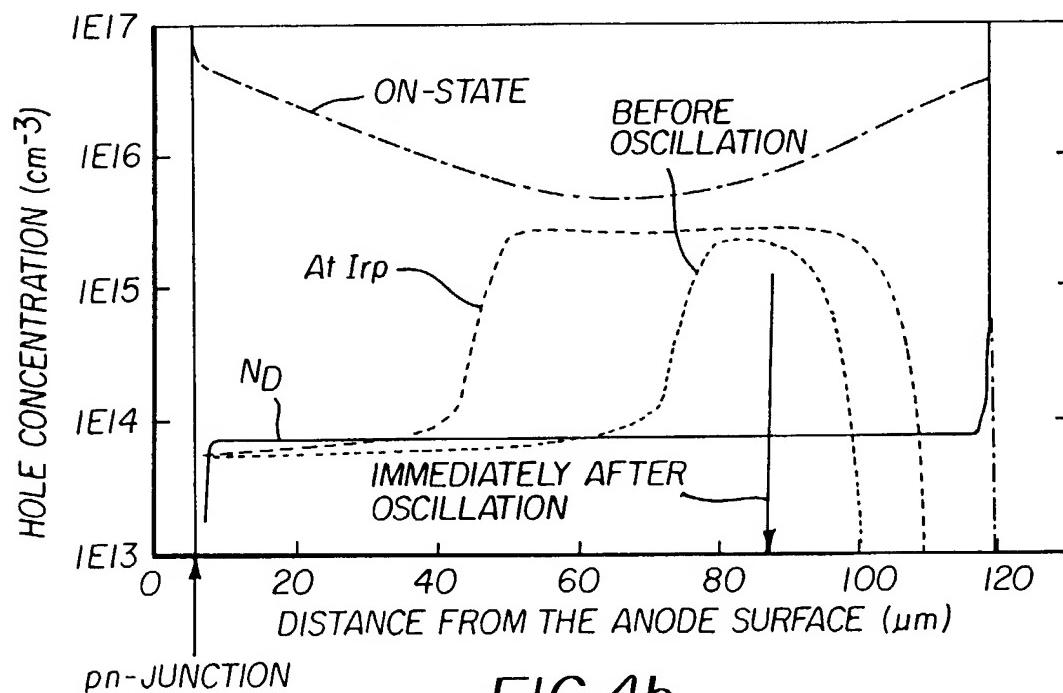


FIG. 4b

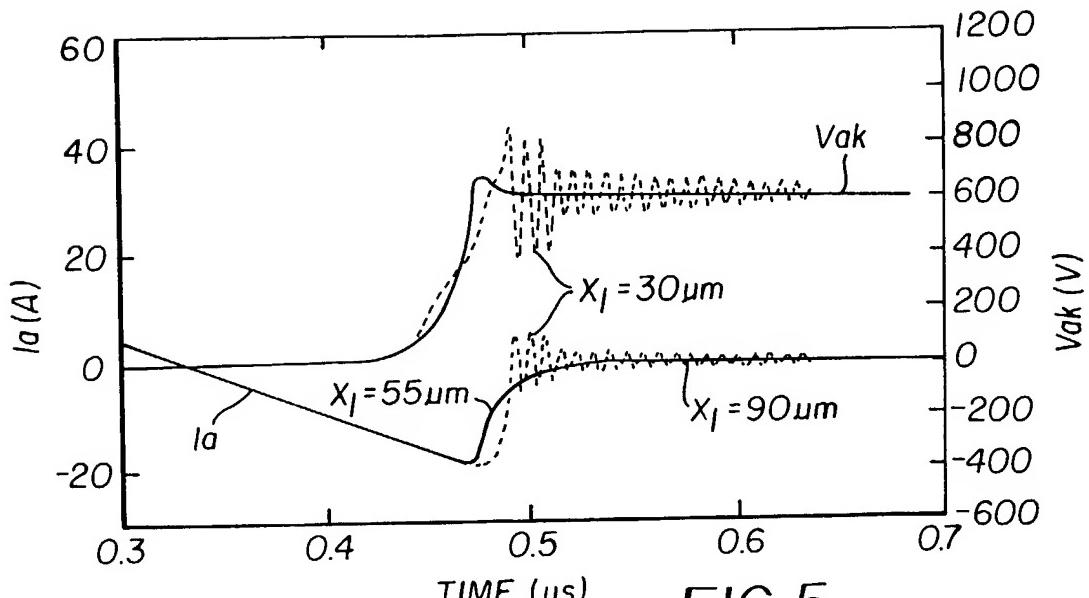


FIG. 5

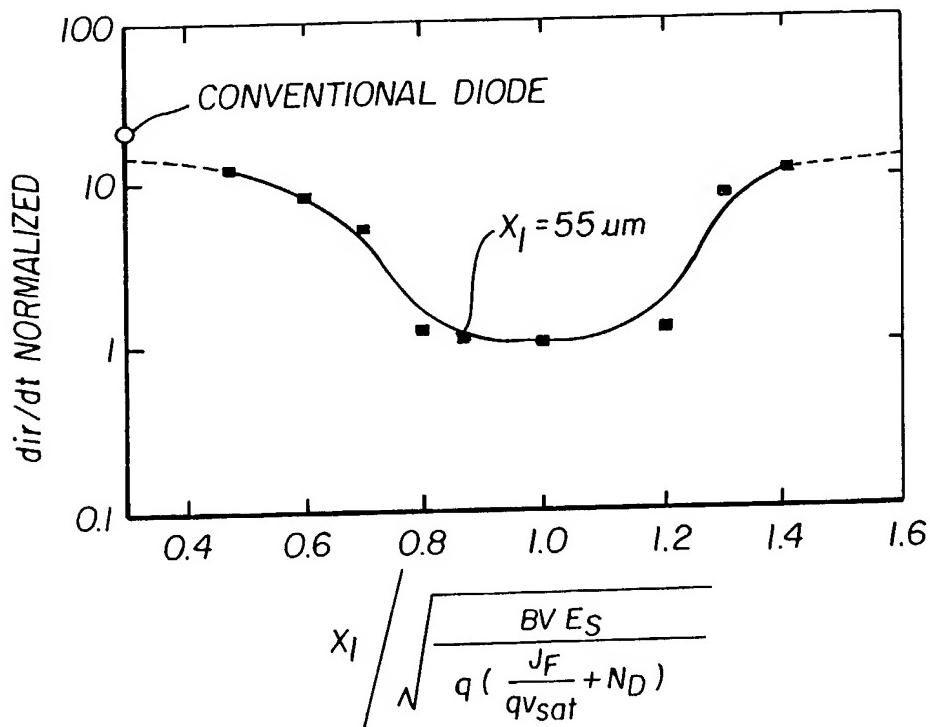


FIG. 6

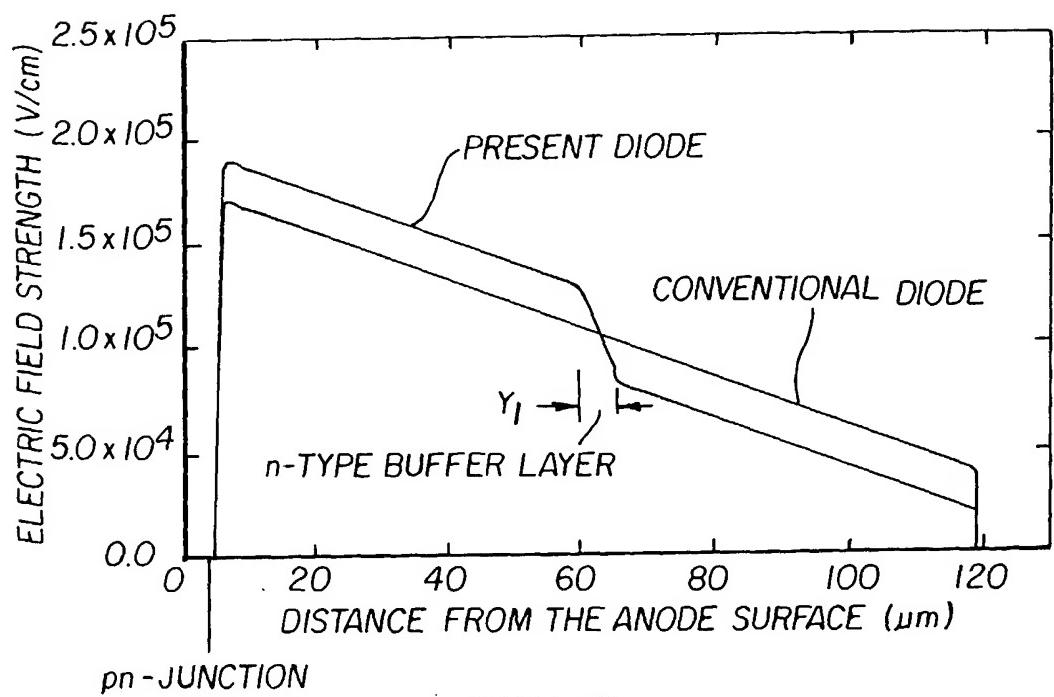
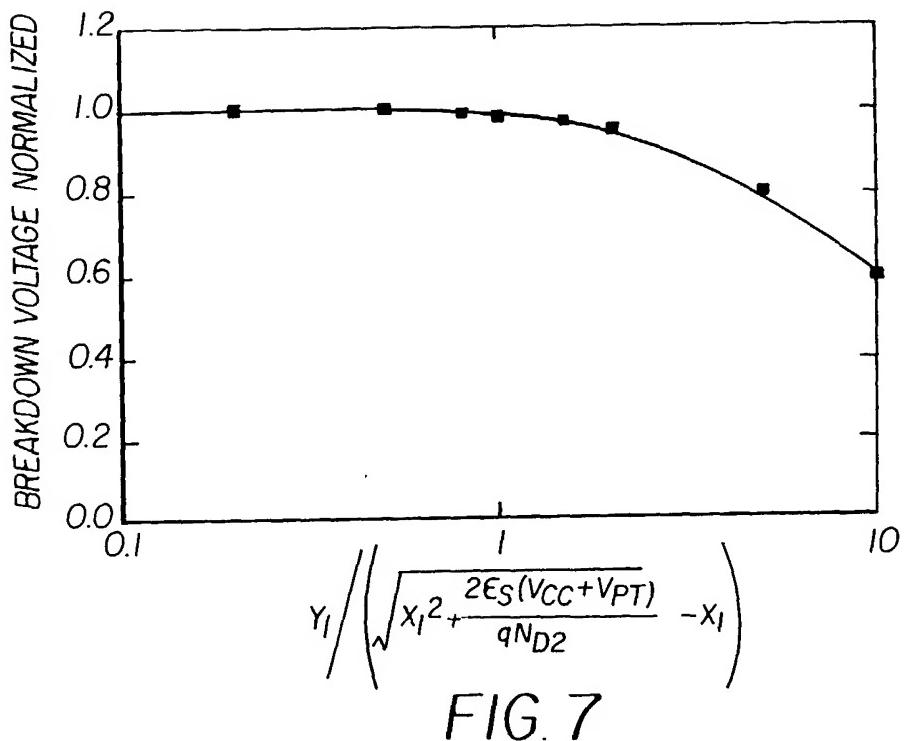


FIG. 8

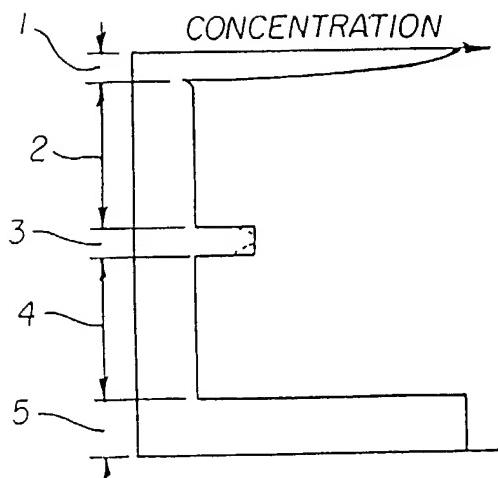


FIG 9

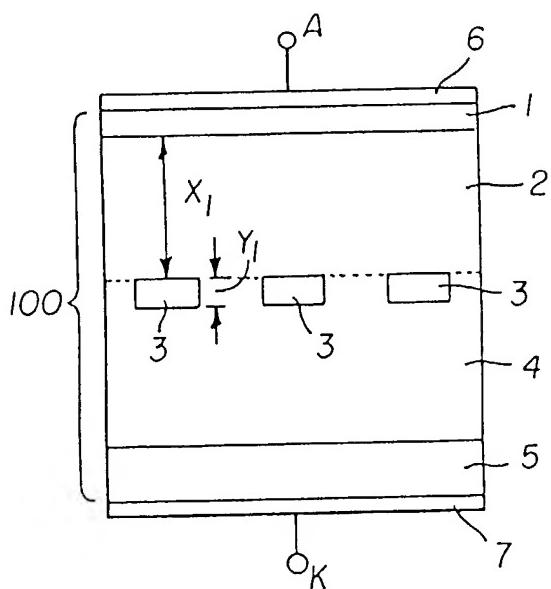


FIG.10

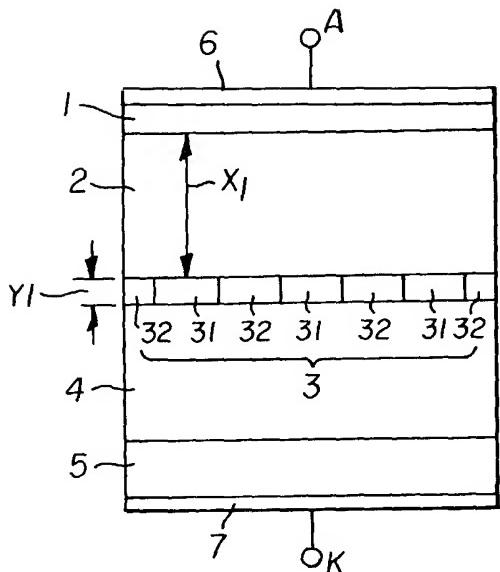


FIG. 11a

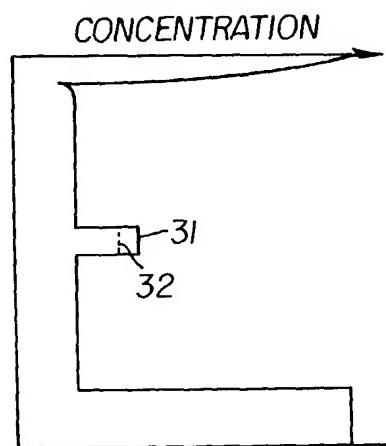


FIG. 11b

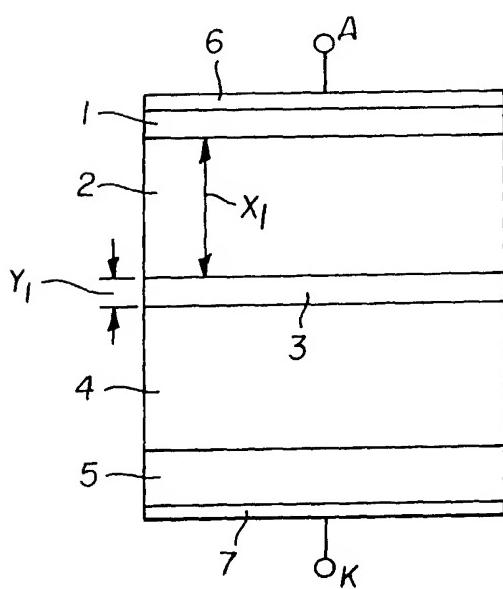


FIG. 12a

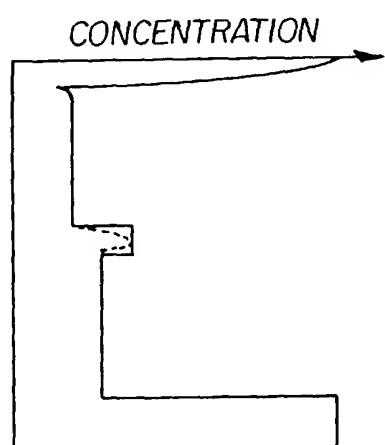


FIG. 12b

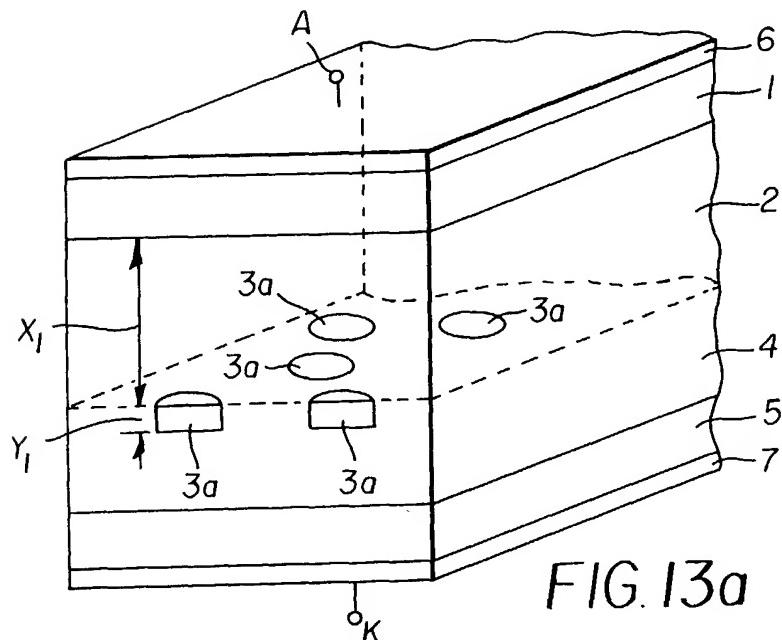


FIG. 13a

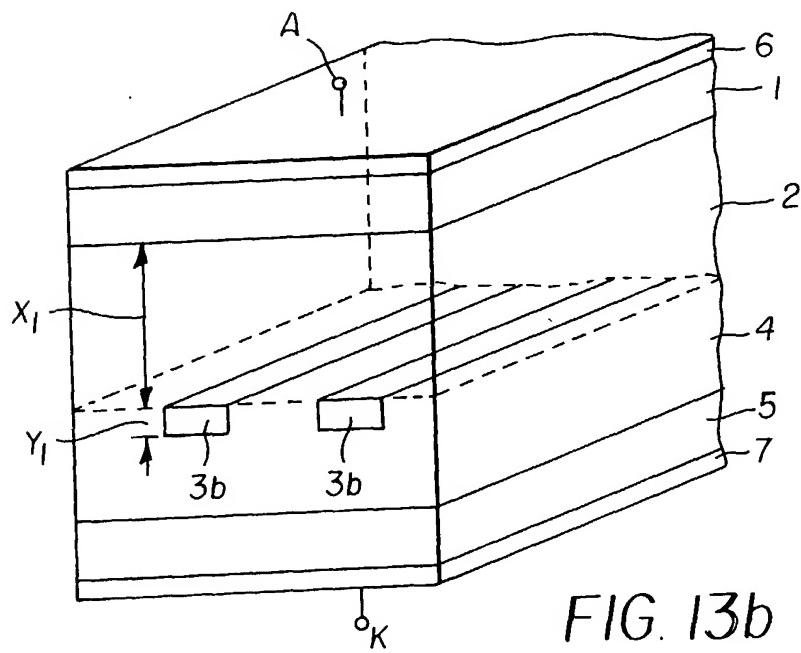
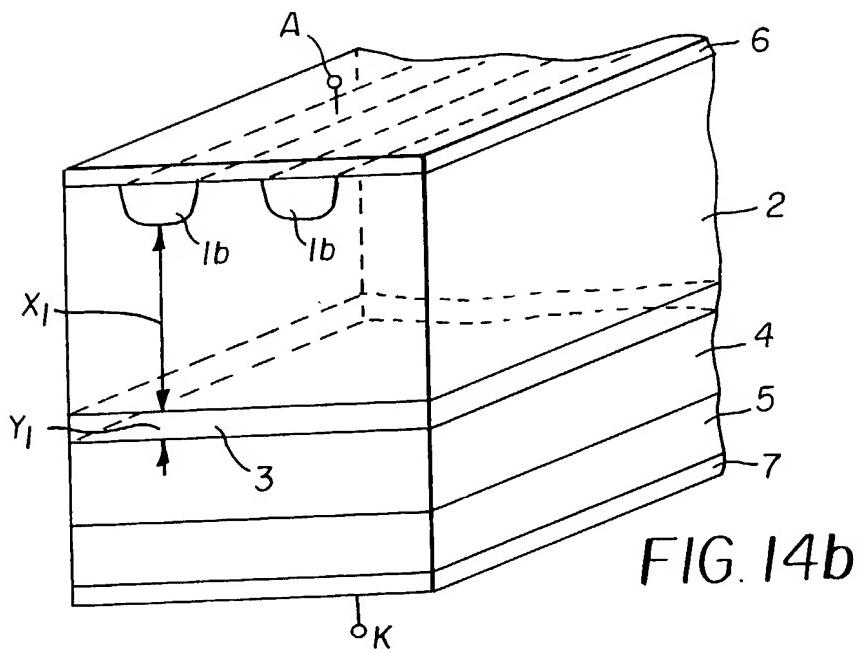
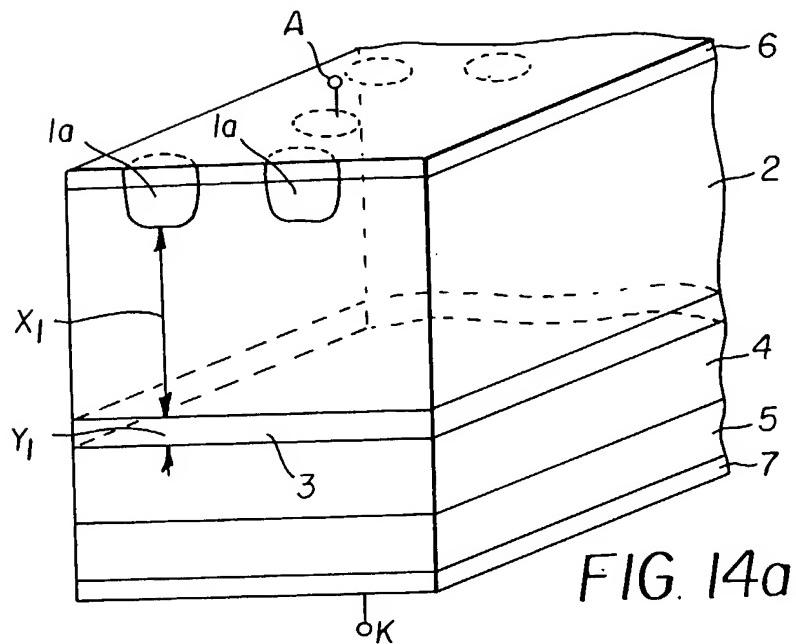
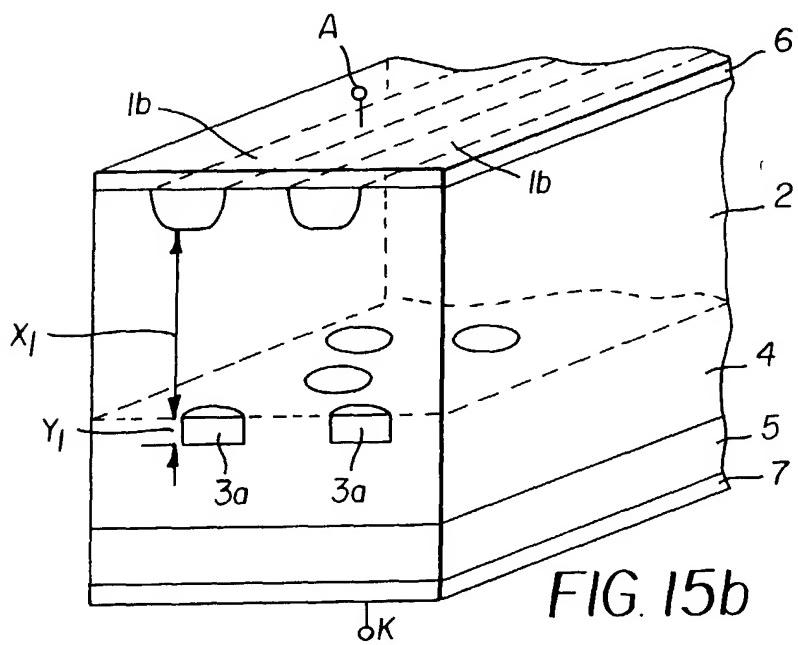
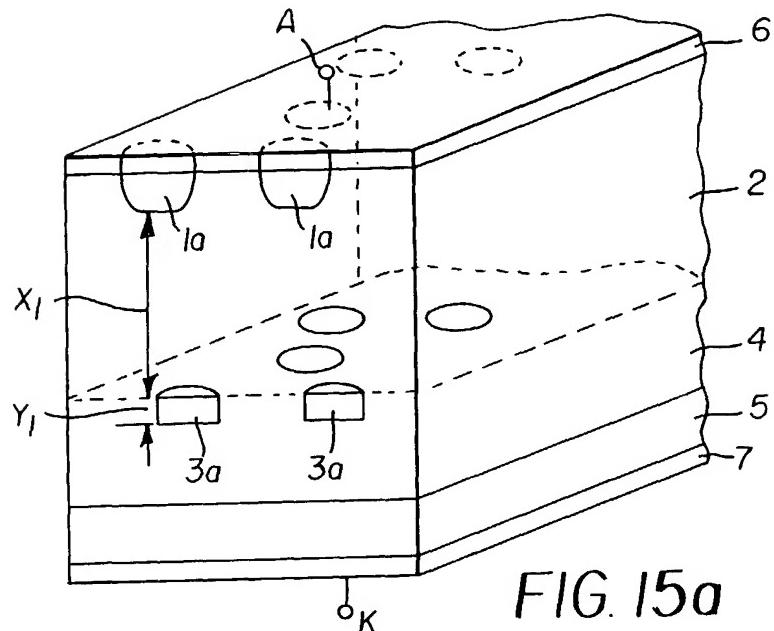


FIG. 13b





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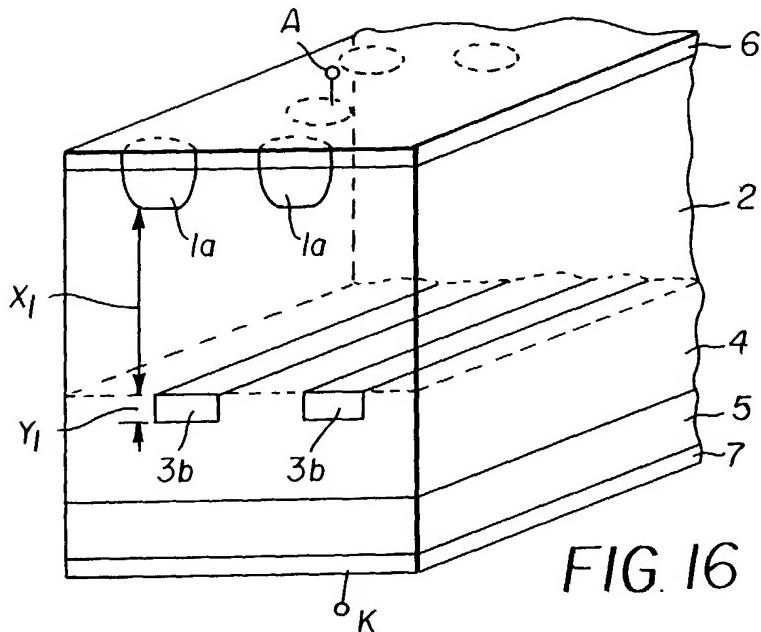


FIG. 16

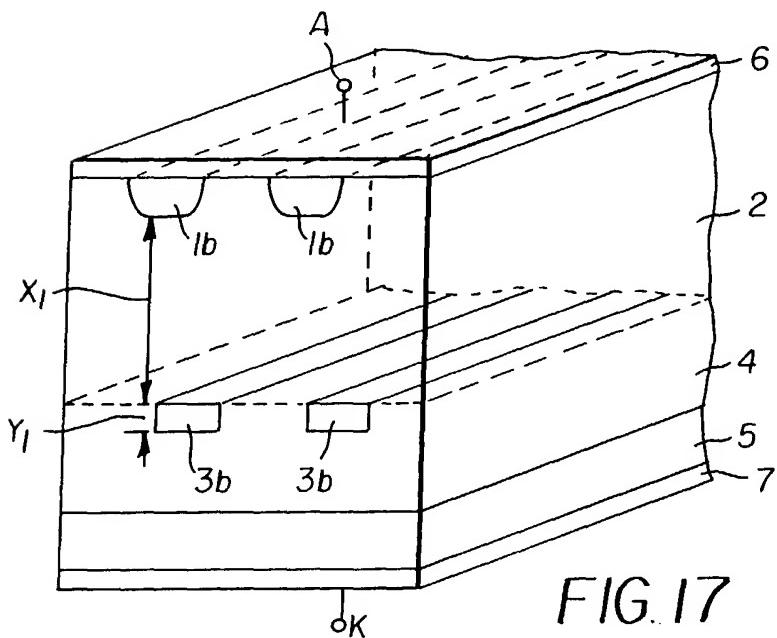


FIG. 17

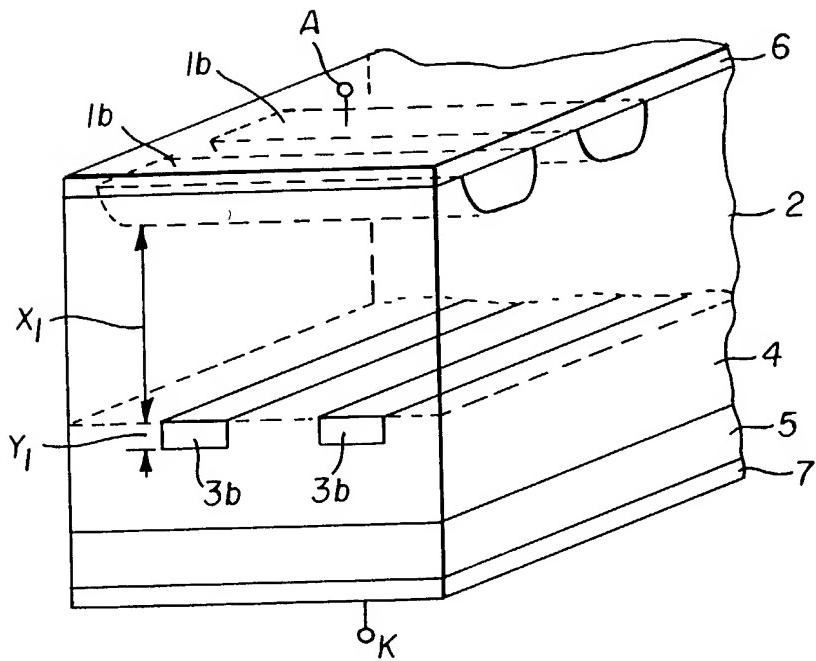


FIG. 18

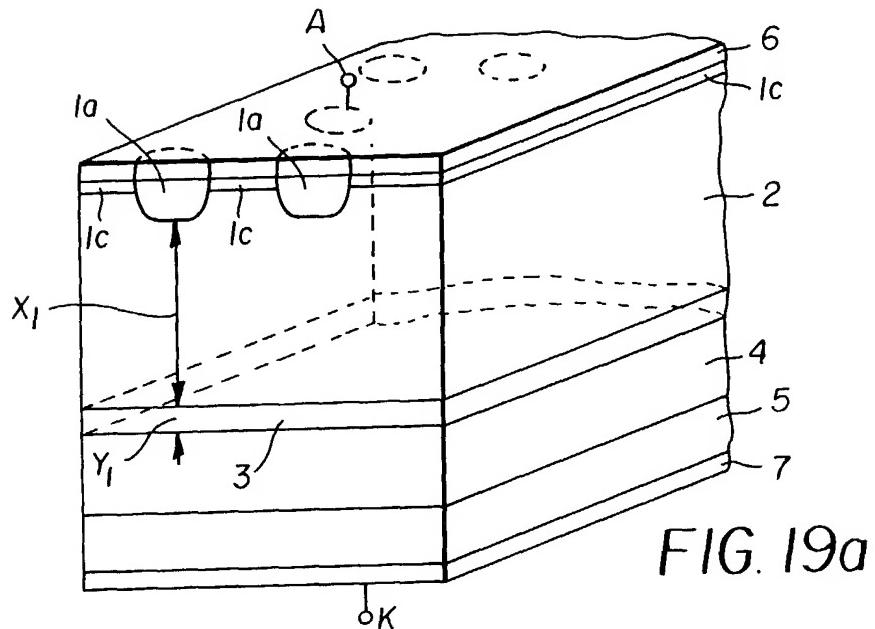


FIG. 19a

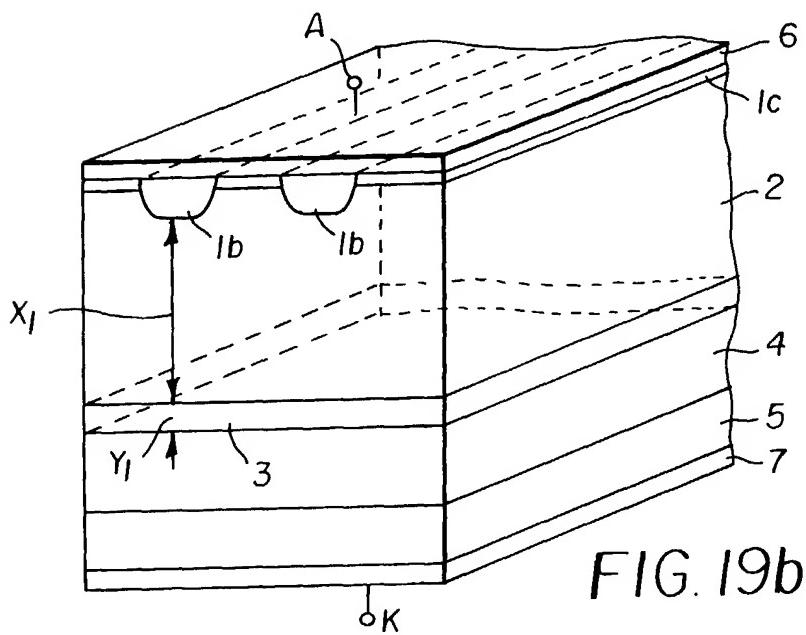


FIG. 19b

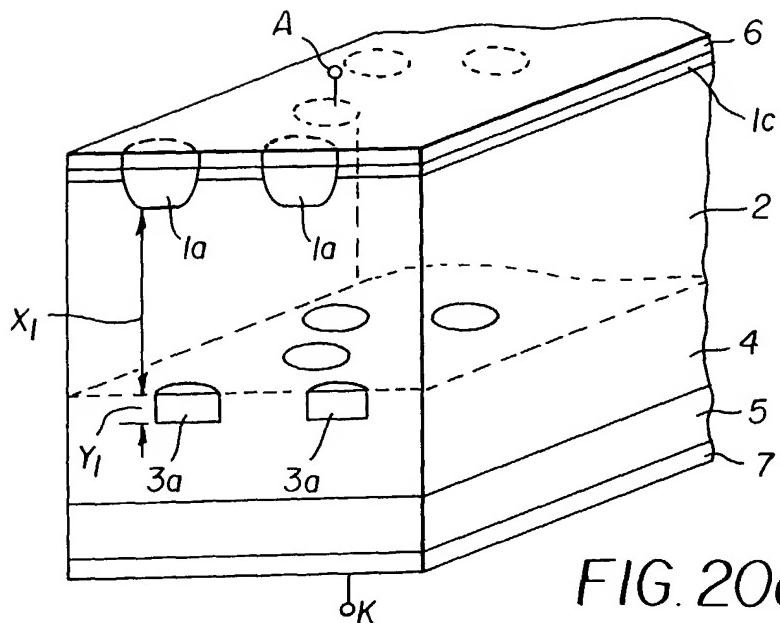


FIG. 20a

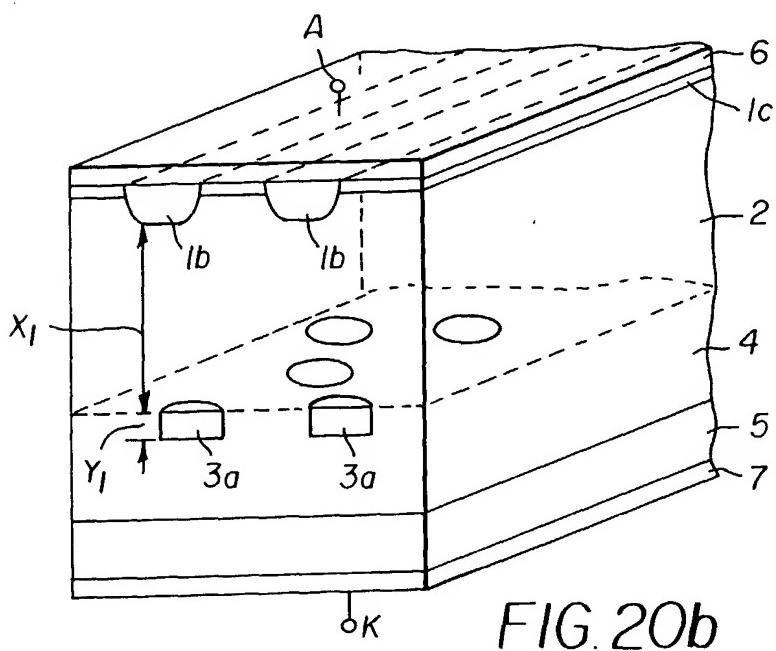


FIG. 20b

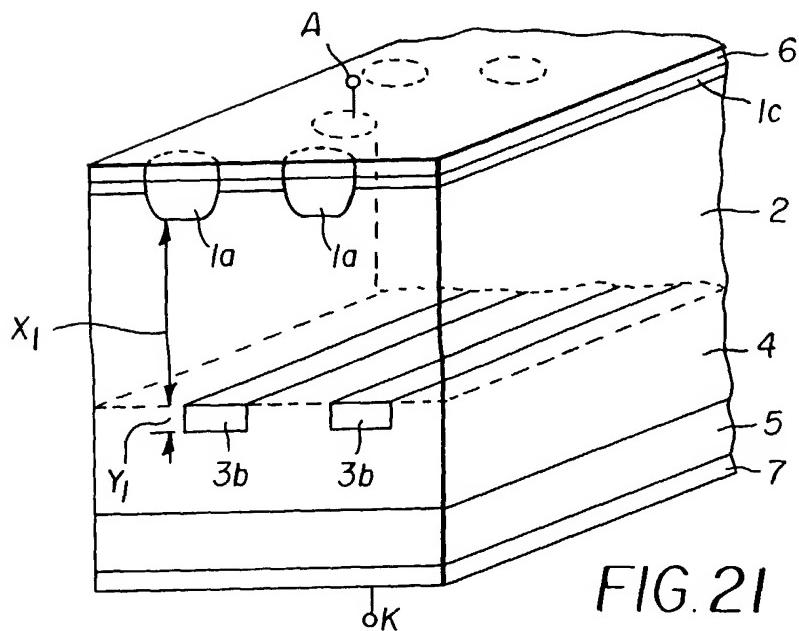


FIG. 21

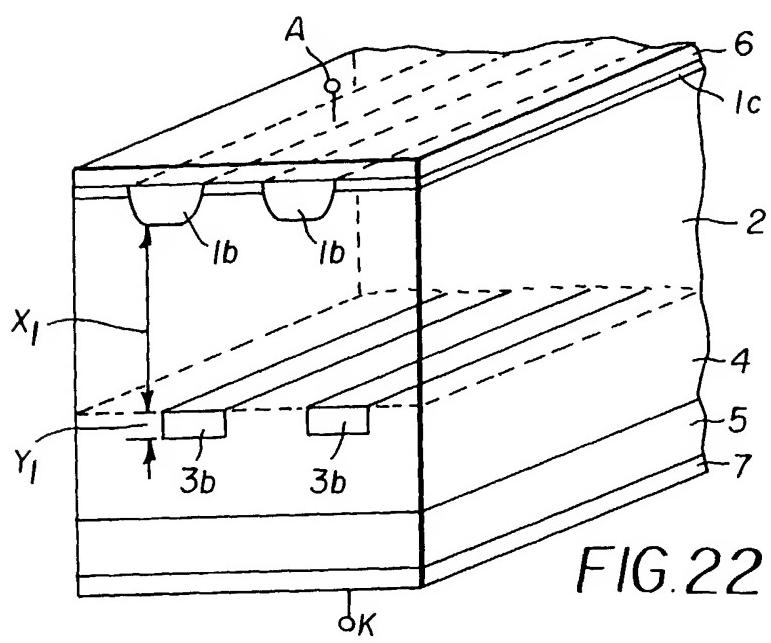


FIG. 22

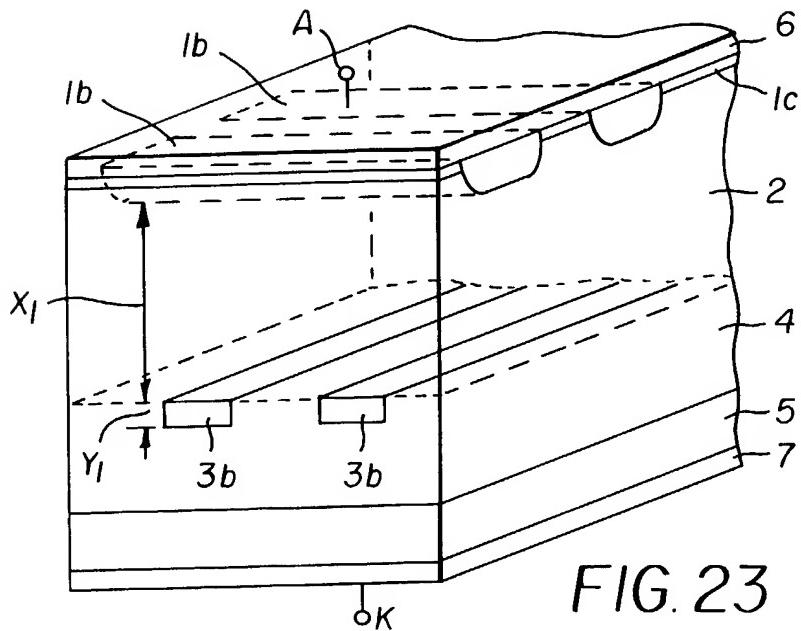


FIG. 23

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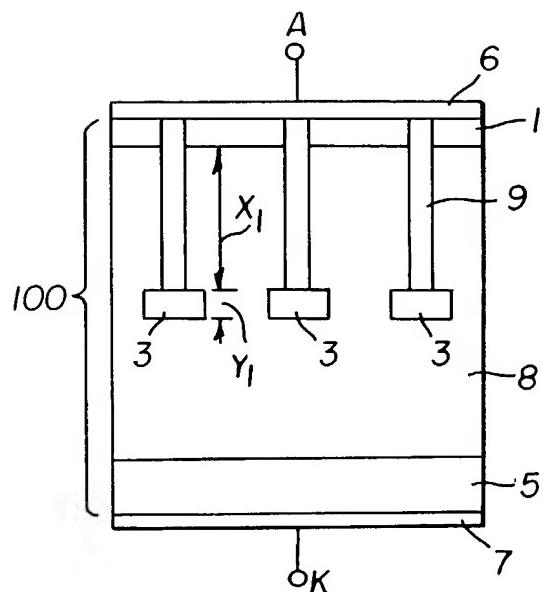


FIG. 24a

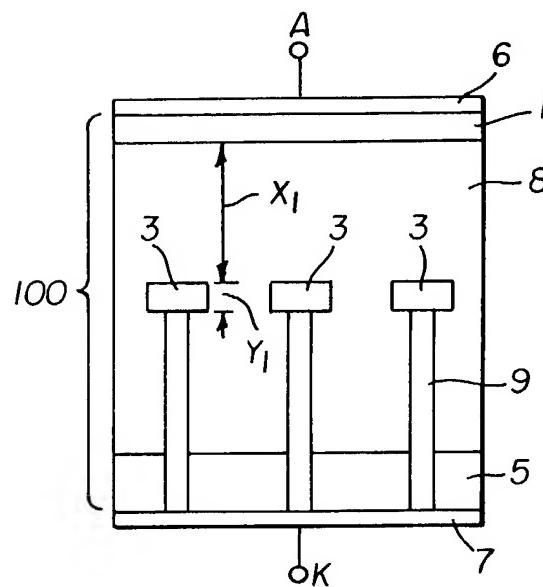


FIG. 24b

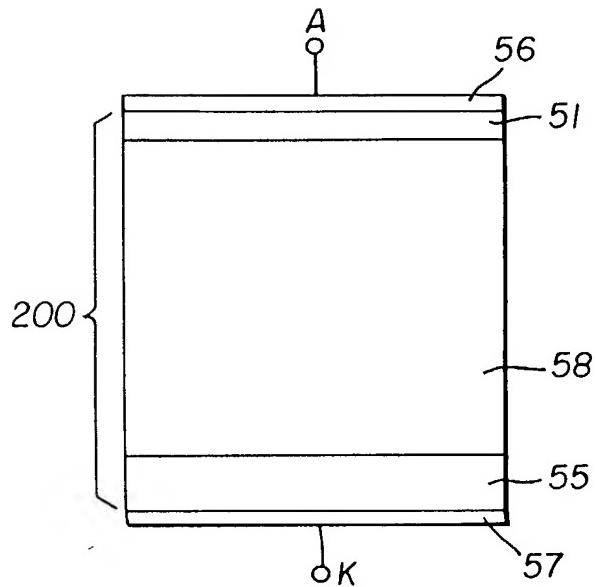


FIG. 25

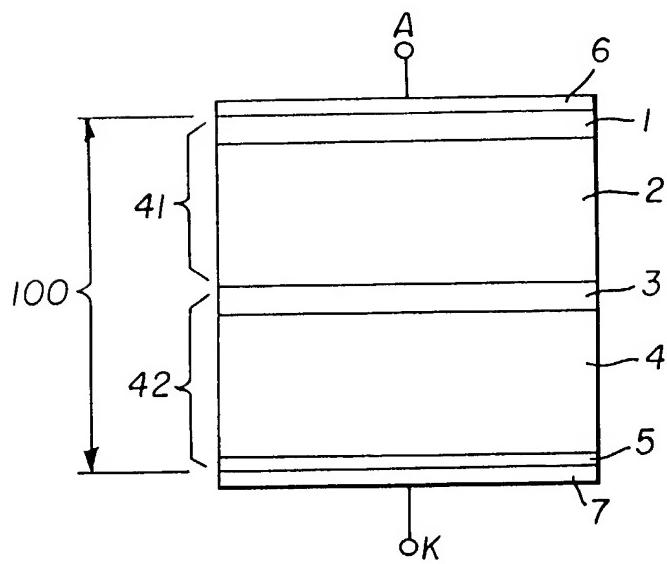


FIG. 26

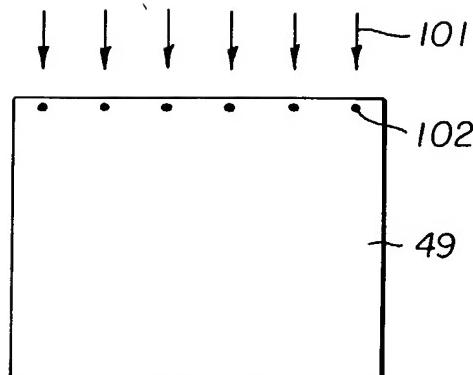


FIG. 27

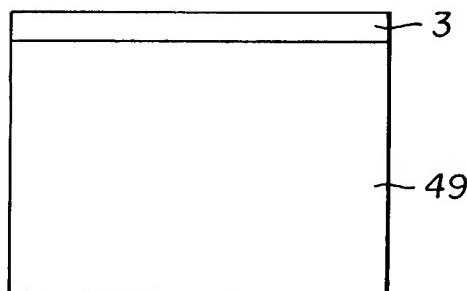


FIG. 28

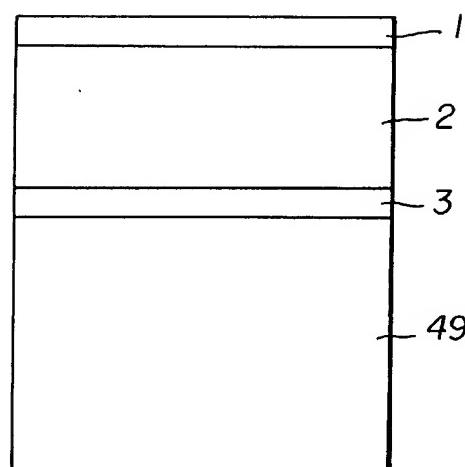


FIG. 30

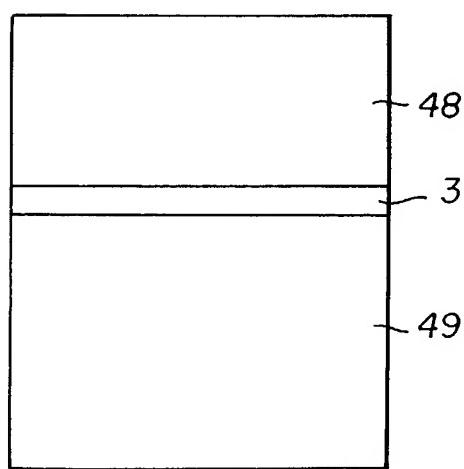


FIG. 29

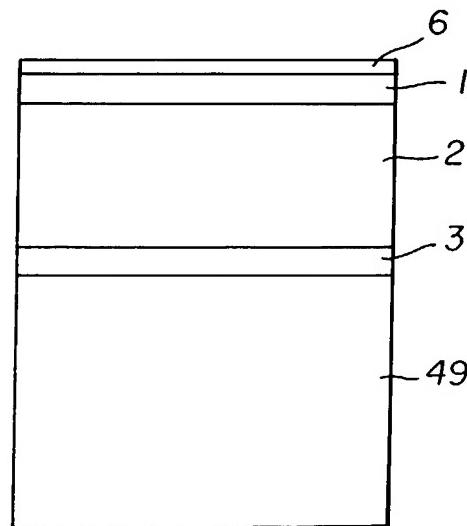


FIG. 31

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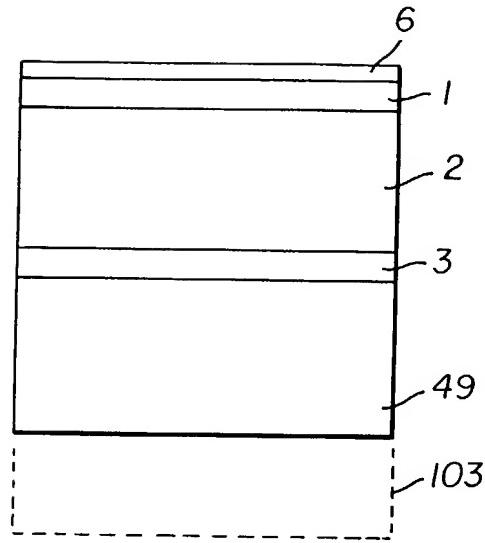


FIG. 32

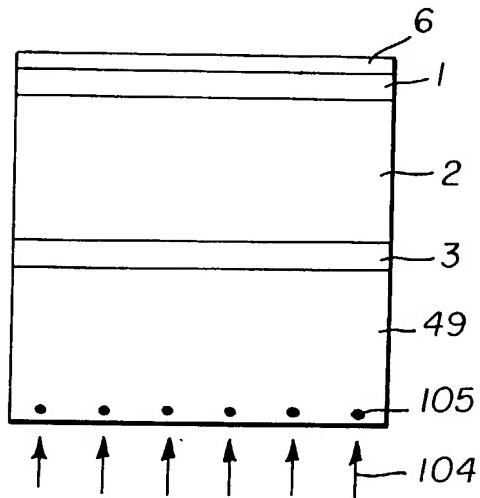


FIG. 33

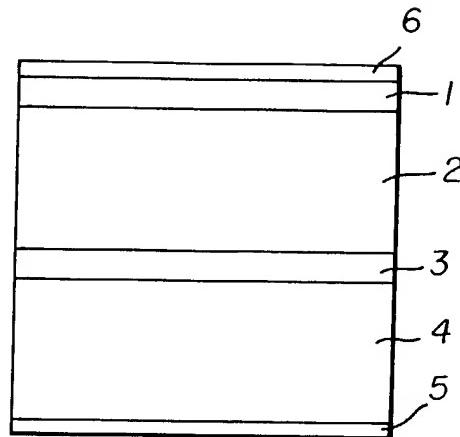


FIG. 34

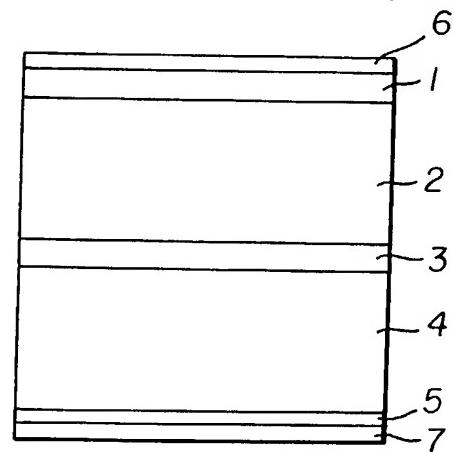


FIG. 35